

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO.

CONT/PRIOR

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| CLASS | |
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SUBCLASS

ART UNIT

EXAMINER

09/643380

711

RSD

2186
2185

D. Tran

APPLICANTS

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TITLE

Method and apparatus for pipelining ordered input/output transactions to coherent memory in a distributed memory, cache coherent, multi-processor system

PTO-2040
12/89

ISSUING CLASSIFICATION

| ORIGINAL | | CROSS REFERENCE(S) | | | | | |
|------------------------------|----------|--------------------|-----------------------------------|--|--|--|--|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS Sheets Drwg. Figs. Drwg. Print Fig. | | CLAIMS ALLOWED Total Claims Print Claim for O.G. | |
| | | | NOTICE OF ALLOWANCE MAILED | |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | ISSUE FEE Amount Due Date Paid | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | ISSUE BATCH NUMBER | |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | |
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